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John Shipp John Edward Shipp (March 20, 1894 – March 7, 1986) was an American speed skater who competed in the 1924 Winter Olympics. In 1924 he finished 14th in the 500 metre competition and 30th in the 1500 metre competition. External links Speed skating 1924 Category:1894 births Category:1986 deaths Category:American male speed skaters Category:Olympic speed skaters of the United States Category:Speed skaters at the 1924 Winter Olympics Category:People from Sandwich, New

Hampshire Category:Sportspeople from New Hampshire1. Field of the Invention This invention relates to a driving power control apparatus in a semiconductor memory device, and more particularly, to a power-down control apparatus that uses current sensing to detect whether or not the semiconductor memory device is in a power-down mode, and sends an auto-power-down command to the semiconductor memory device if the semiconductor memory device is not in the power-down mode. 2. Discussion of the Related Art It is a common practice to design a semiconductor memory device with an automatic power-down mode. The automatic power-down mode enables the semiconductor memory device to be powered down for a long period of time to lower power consumption. However, the automatic power-down mode is not activated until the power source voltage falls below a specified voltage. When a power-down command is supplied during the power-down mode, the semiconductor memory device does not perform the read/write operation, but merely returns to a normal operation mode. After the power-down command is supplied to the semiconductor memory device, the semiconductor memory device does not perform the normal operation mode until the power source voltage exceeds the specified voltage and returns to the power-down mode. Thus, the power-down mode is not executed until a predetermined period of time has passed from when the power-down command was supplied to the semiconductor memory device. That is, power is not completely turned off until the power source voltage falls below the specified voltage. Accordingly, the semiconductor memory device will be always powered up for a predetermined period of time. As shown in FIG. 1, a semiconductor memory device 10 may be composed of a plurality of power domains. The power domains are divided into internal and external power domains. Each power domain may have a power-down mode, a normal operation mode, and a power-up mode. The external power domain 82157476af